

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshiyuki NAKAMURA

Examiner: Unassigned

Serial No: 09/965,565

Art Unit: 2133

Filed: Sept. 27, 2001

Docket: 14945

For: SEMICONDUCTOR APPARATUS
FOR PROVIDING RELIABLE DATA
ANALYSIS OF SIGNALS

Dated: June 16, 2004

Confirmation No. 3054

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

RESPONSE TO REQUIREMENT FOR RESTRICTION

Sir:

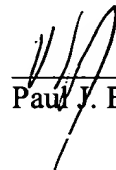
Pursuant to the Restriction Requirement imposed in the Official Action dated May 17, 2004, applicant provisionally elects the claims of Group II, i.e., Claims 5 and 6, for continued prosecution herein.

Claims 1-14 are present in the above-captioned application and have been subjected to restriction under 35 U.S.C. §121. Specifically, the Official Action avers that the following inventions are present in the claims:

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 16, 2004.

Dated: June 16, 2004



Paul J. Esatto, Jr.